Also published as:

JP3660391 (B2)

EP0684637 (A2)

EP0684637 (A3)

RR100272138 (B1)

SEMICONDUCTOR DEVICE AND ITS MANUFACTURE

Publication number: JP8045927 (A)

Publication date:

1996-02-16

Inventor(s):

KASAI YOSHIO; SUZUKI TAKASHI; TSUDA TAKANORI;

MIKATA YUICHI; AKAHORI HIROSHI; YAMAMOTO AKITO +

Applicant(s):

TOSHIBA CORP +

Classification:
- international:

H01L27/04; H01L21/28; H01L21/318; H01L21/334; H01L21/822; H01L21/8242; H01L27/108; H01L27/04;

H01L21/02; H01L21/70; H01L27/108; (IPC1-7): H01L21/318;

H01L21/822; H01L21/8242; H01L27/04; H01L27/108

HUILZ 1/022; HUILZ 1/0242; HUILZ 1/04, HUIL

- European:

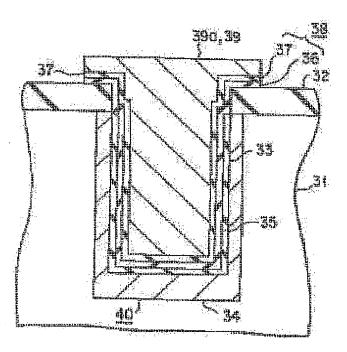
H01L21/28E2C2; H01L21/318B; H01L21/334C

Application number: JP19950121030 19950519

Priority number(s): JP19950121030 19950519; JP19940115293 19940527

Abstract of JP 8045927 (A)

PURPOSE: To obtain a semiconductor device fitted with a composite insulating film which is high in dielectric strength, prevented from deteriorating in reliability, and lessened in thickness by a method wherein the semiconductor device is equipped with a semiconductor layer and a thermal nitride film which is directly provided onto the semiconductor layer and whose oxygen content is below a specific value in oxygen concentration. CONSTITUTION:A semiconductor device is equipped with a semiconductor layer 34 and a thermal nitride film 35 which is directly provided onto the semiconductor layer 34 and of oxygen concentration 1.36X10<15> atoms/cm<2> or below. For instance, a trench 33 is cut in a semiconductor wafer 31, and an impurity diffusion layer 34 is formed on the inner surface of the trench 33.; Then, the semiconductor wafer 31 is thermally treated in an NH3 gas atmosphere of reduced pressure, whereby not only a natural oxide film is removed but also a thermal nitride film 35 is formed, and then a CVD silicon nitride film 36 is formed. Thereafter, a silicon oxide film 37 is formed, and a composite insulating film 38 of three-layered structure composed of the silicon oxide film 37, the CVD silicon nitride film 36, and the thermal nitride film 35 is formed, furthermore an electrode 39 formed of polycrystalline silicon film 39a is provided, and thus a capacitor 40 is formed.



Data supplied from the espacenet database — Worldwide